	OIPE				
/	S. C.		Application Number	10/074,732	
3	FEB 2 2 2005		Filing Date	February 13, 2002	
/	TO ANSMITTAL FORM		First Named Inventor	Leo Mathew et al.	
	FORM		Group Art Unit	2815	
			Examiner Name	Paul E. Brock II	
	Total Number of Pages in this Submission	7	Attorney Docket Number	SC11805TP	

			ENC	LOSURES		(check a	ll that apply)
X Fee Tra	ansmittal Fo	orm		ssignment Papers or an Application)			llowance nunication to Group
	Fee Attach	ed		awing(s)		Appeal	Communication to Board eals and Interferences
Amend	ment/Reply	1	Lic	censing-Related papers		Appeal	Communication to Group  Notice, Brief, Reply Brief)
	After Final		Pe	etition			etary Information
	Affidavits/D	Declaration(s)		tition to Convert to a ovisional Application		Status I	Letter with appropriate copies
Extensi	ion of time	Request	Po	ower of Attorney, Revocation, lange of Correspondence			nclosure(s) (please identify below) ponse to Restriction Requirement
Expres	s Abandoni	ment Request		dress		Ass	cociate Power of Attorney turn Post Card
		sure Statement, ferences CA-CH	Te	erminal Disclaimer			
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Incomp	lete Applic	ation	Remarks				
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			OF APPL	LICANT, ATTORNEY,	OR AC	ENT	
Firm or Individual	Robert I	L. King			Regis	tration No.	30,185
Signature	/	Robert o	L.	King			
Date		Februa 18	200	<u></u>			
		J ,	CERTIFI	CATE OF MAILING			
I hereby certify th first-class mail, in below:	at this corre an envelop	espondence is being pe addressed to: Co	deposited wi mmissioner f	ith the United States Postal or Patents, PO Box 1450, A	Service lexandr	with sufficier ia, VA 22313	nt postage thereon, as -1450 on the date listed
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Signature						Date	

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<u> </u>			Complete if Known	
FEE 2 2 2005 W		Application Number	10/074,732	
I PED TEDANICMITT	AL	Filing Date	February 13, 2002	
TRABELENT fees are subject to ar		First Named Inventor	Leo Mathew et al.	
TRADED it fees are subject to ar	nnual revision	Examiner Name	Paul E. Brock II	
		Group Art Unit	2815	
TOTAL AMOUNT OF PAYMENT	(\$) 180	Attorney Docket No.	SC11805TP	

METHOD C	F PAYMENT			FE	E CAL	CULAT	ON (continued	1)
1. X The Commissioner is hereby authoriz	ed to charge indicated fees and		DITIONA					
credit any overpayment to:  Deposit Account Number	503079		rge <u>tit</u> y		nali <u>itity</u>			
Deposit Account Name	Freescale Semiconductor,	Fee	Fee	Fee	Fee			
	Inc.							
		Code	(\$)	Code	(\$)		Fee Description	
X Charge Any Additional Fee required to	under 37 CFR 1.16 and 1.17	1051	130	2051	65	Surchar	ge – late filing fee or	oath
Applicant claims small entity status.	See 37 CFR 1.27	1052 1053	50 130	2052 1053	25 130		ge – late Provisional glish specification	filing
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2. Payment Enclosed:		1804	920*	1804	920*	Requestir Examine	ng publication of SIR pricer action	or to
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1. BASIC FILING FEE							for reply within fourth m	
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1002 330 2002 165 1003 520 2003 260	Design filing fee Plant filing fee	1501	1300	2501	650		to revive - unintentio sue fee (or reissue)	
1004 750 2004 375	Reissue filing fee	1502	470	2502	235	-	ssue fee	
1005 160 2005 80	Provisional filing fee	1503	630	2503	315	Plant iss	ue fee	
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	SUBTOTAL (1) (\$)	1807	50	1807	50		ing fee under 37 CFR	1.17(q)
2. EXTRA CLAIM FEES		1806	180	1806	180		sion of IDS	180
Previously Paid**	Extra Fee from Claims below Fee Paid	8021	40	8021	40	per prope	ng each patent assign arty (times number of pro	perties)
Total Claims - 20 Independent Claims - 3	= X 18 = X 84 =	1809	750	2809	375	-	submission after final (37 CFR § 1.129(a)	
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'	SUBTOTAL (2) (\$)	* Reduc	ed by Bas	sic Filina	Fee paid	SUF	STOTAL (3)	\$) 180
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SUBMITTED BY							Complete (if	applicable)
Name (Print/Type) Robert L	. King	Registra	ation No.	30,1	85		Telephone	(512) 996-6839
Signature	Robert L. Kin	Date:			//8/	05		
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Application No.: Leo Mathew et al.

10/074,732

Group Art Unit: Examiner: Docket No .:

Paul E. Brock II

Date Filed:

February 13, 2002

SC11805TP

2815

METHOD OF FORMING A VERTICAL DOUBLE GATE SEMICONDUCTOR DEVICE

Certificate of Transmission under 37 CFR 1.8



I hereby certify that this correspondence is transmitted to the Patent and Trademark Office.		facsimil
on		
Signature		
Pat Thomas	-	
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## **INFORMATION DISCLOSURE STATEMENT (IDS)**

Commissioner For Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

In accordance with 37 C.F.R. §1.56 and in compliance with 37 C.F.R. §§1.97 and 1.98, the references listed on attached Form PTO/SB/08 and/or subsequently identified herein, are being submitted herewith for consideration by the United States Patent and Trademark Office. The Office hereby waives the requirement under 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003. See 37 CFR 1.491(b).

	• •	
I. <u>COPIES</u> a.⊠ b.□	A legible copy of (i) each U.S. and foreign patent; (ii) each listed; and (iii) all other information or that portion which call any patents, publications or other information which are herewith were previously cited by or submitted to the PT been relied upon for an earlier filing date under 35 U.S.C. §	used it to be listed, is included herewith. e listed on PTO/SB/08 which are not enclosed O in one of the following applications which has
	U.S. Serial Number	U.S. Filing Date

CONCISE EXPLANATION OF THE RELEVANCE (check at least one box) 11. a. 🔯 Except as may be indicated below in (b) of this section, all of the patents, publications or other information are in the English language (concise explanation not required). A concise explanation of the relevance of all patents, publications or other information listed that is not in the b. □ English language is as follows: The following additional information is provided for the Examiner's consideration: c. 🗌 CROSS REFERENCE TO RELATED APPLICATION(S) III.

The Examiner is advised that the following co-pending application(s) contain(s) subject matter that may be related to the present application. By bringing this (these) applications to the Examiner's attention, Applicant(s) does(do) not waive the confidentiality provisions of 35 U.S.C. §122.

Serial No.

Filing Date

Art Unit

10074732 02/24/2005 DTESSEH1 00000025 503079

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## **FEES**

IV. 🔲	THIS IDS a b c d	within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) (37 C.F.R. §1.97(b)(1)). No fee or statement is required. within three months of the date of entry of the national stage as set forth in § 1.491 in an international application (37 C.F.R. §1.97(b)(2)). No fee or statement is required. before the mailing date of a first Office Action on the merits (37 C.F.R. §1.97(b)(3)). No fee or statement is required. before the mailing date of a first Office Action after the filing of a request for continued examination under § 1.114 (37 C.F.R. § 1.97(b)(4)). No fee or statement is required.
V. 🗖	before th	E IS BEING FILED UNDER 37 C.F.R. §1.97(c): (check one box)  e mailing date of any of a Final Office Action under 37 C.F.R. §1.113, a Notice of Allowance under 37 C.F.R. or an action that otherwise closes prosecution in the application (See 37 C.F.R. §1.97(c)).  No statement; therefore, charge Deposit Account 503079, Freescale Semiconductor, Inc. the fee set forth in 37 C.F.R. §1.17(p).  See the statement below. No fee is required.
VI. 🛛		S IS BEING FILED UNDER 37 C.F.R. §1.97(d): ore payment of the issue fee and is accompanied by the following: a statement under 37 C.F.R. §1.97(e) as provided below; and charge Deposit Account 503079, Freescale Semiconductor, Inc. the petition fee set forth in §1.17(p).
VII. ⊠		ersigned hereby states that each item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of IDS; or no item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application, and to knowledge of the person signing the statement after making reasonable inquiry, no item of information contained in the IDS was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this statement, or some of the items of information contained in the IDS were cited in a communication from a foreign Patent Office. As to this information, the undersigned states that each item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this IDS. As to the remaining information, the undersigned hereby states that no item of this remaining information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application or, to the knowledge of the person signing the statement after making reasonable inquiry, no item of information contained in the IDS was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this statement.
VIII.	PAYMEN 	A check in the amount of is enclosed for the above-identified fee(s).  Please charge Deposit Account No. 503079, Freescale Semiconductor, Inc. in the amount of \$180.00 for the above-indicated fee(s).  If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.  Two Copies of this paper are attached for Deposit Account charges and debits.

The above references are being cited only in the interests of candor and without any admission that they constitute statutory prior art or contain matter which anticipates the invention or which would render the same obvious, either singly or in a combination, to a person of ordinary skill in the art.

If the Examiner has any questions concerning this IDS, he/she is requested to contact the undersigned. If it is determined that this IDS has been filed under the wrong rule, the PTO is requested to consider this IDS under the proper rule (with a petition if necessary) and charge the appropriate fee to Deposit Account No. 503079, Freescale Semiconductor, Inc.

Respectfully submitted, Leo Mathew et al.

Robert L. King

Attorneyfor Applicant(s)

Reg. No. 30,185

Tel. (512) 996-6839

FREESCALE SEMICONDUCTOR, INC.

Customer Number 23125

**Enclosures:** 

✓ PTO/SB/08

References CA-CH

Other:



## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)
Sheet 1 of 2

	Complete If Known	
Application Number	10/074,732	
Filing Date	2/13/2002	
First Named Inventor	Leo Mathew et al.	
Gro-up Art Unit	2815	
Examiner Name	Paul E. Brock II	
Attorney Docket Number	SC11805TP	

			U. S. PATENT DOCU	MENTS	
Examiner Initials*	Cite No. 1	Document Number Number -Kind Code² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	CA	US - 6,330,184	12-11-2001	White et al.	
	СВ	US - 6,097,065	08-01-2000	Forbes et al.	
	CC	US - 6,150,687	11-21-2000	Noble et al.	
	CD	US - 6,011,725	01-04-2000	Eitan	
	CE	US - 5,804,848	09-08-1998	Mukai	
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		FOREIGN PAT	TENT DOCUMENT	S		
Examiner Initials*	Cite No. 1	Foreign Patent Document Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Τ¢
	CF	DE 101 25 967 C1	05-29-2001	Schlösser et al.		
		*				

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	CG	CHAN et al.; "A True Single-Transistor Oxide-Nitride-Oxide EEPROM Device"; IEEE Electron Device Letters; March 1987; pp 93-95; Vol. EDL-8, No. 3.	
•	СН	GONZALES SR., "A Dynamic Source-Drain Extension (DSDE) MOSFET using a Separately Biased Conductive Spacer"; Semiconductor Device Research Symposium International; December 5-7, 2001; pp. 645-648; IEEE.	
			1.
			ļ.,
Examiner Signature	<u> </u>	Date Considered	ل.

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation, if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>&</sup>lt;sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English Language Translation is attached.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.